Jetter Nano Series PLC

HMI Factory Setting:

Baud rate: 9600. 8. Even. 1(RS-232)

Controller Station Number: 0 (no PLC station number in protocol, one HMI to one PLC connection)

Control Area / Status Area: WR0/WR10

Connection

. RS-232 (DOP-A/AE/AS, DOP-B Series)			
DOP series	Controller		
9 pin D-SUB (RS-232)	9 pin D-SUB male (RS-232)		
RXD (2)	(2) TXD		
TXD (3)	(3) RXD		

Definition of PLC Read/Write Address

a. Registers

Time	Format	Deed (M/site Dense	Data	
Туре	Word No. (n)	Read/Write Range	Length	Note
16 Bits Register	WRn	WR 0 – WR 32767	16 Bits	<u>5</u>
32 Bits Register	Rn	R 0 – R 32767	24 Bits	<u>3, 6, 7</u>

b. Contacts

Туре	Format Word No. (n) Bit No. (b)	Read/Write Range	Note
Input Relay	Inbb	1 101 - 1 3208	PLOT in PLOT in
Output Relay	Onbb	O 101 – O 3208	bround bround
Flag Relay	Fb	F0 - F32767	PLO'" PLO'"

- 1) In general, every register occupies a maximum 24 Bits. However, some registers occupies only 8 Bits.
- 2) Jetter Nano Series PLC requires longer time at initial start, therefore it is recommended to set startup delay time greater than 10 (s).

figuration			
tandard Communication Prin	t Default Others	PLON' PLON'	PLO'N PLO'N
Standard		Control Block	
Project Name		Address	WR0
HMI	- CA. ³⁰ CA. ³	Length	0
HMI	And Area	Sample cycle	300 💽 (ms)
DOP-A57CSTD 256 Col	ors 🗸	Auto reset flags	
Base Port Controller	Provide Street	-Status Block	
🍠 Nano Series	×	Address	WR10
Security		Optimize Type	
Password	12345678	 Dynamic 	◯ Static
Starting Level	0		
Retained data location	SRAM	Upload/Download	0.54
			○ Ethernet
The size of writing to USB	Default	O PC COM Port	COM1
Show warning message if acces		Startup Delay Time	10 (s)
Buzzer ON/OFF	C. Lowelly	Clock Macro Delay Time	100 (ms)
Enable USB updating chec	k CAN STORY	Clock Macro Priority	Low 💌
Insufficient password leve		Background macro update cy	rcle 1 💽 Lines
Varify the download pass	bow		

- 3) When the register R is used for Double Word device, please set the format as signed format. (The default format in Screen Editor is signed format.)
- 4) Please be aware the pin definition for RS232 in this PLC series is different than the standard RS232, do not mistake.
- 5) **WR** only occupies Bit0~Bit15 of every register.
- V1.01 Revision November, 2011

DUP Series HMI Connection Manual

- 6) **R** occupies 24 Bits of every register and Bit24~Bit31 set to 0 by default setting.
- 7) Decimal notation range from -8388608 to +8388607; hexadecimal notation range from 0x000000 to 0xFFFFFF.
- 8) The difference between WRn and Rn register:
 - 1. When using devices that the data length is in Word, only Bit $0 \sim 15$ are valid for both of WRn and Rn registers.
 - When using devices that the data length is in Double Word, if the read/write address format is set to WRn, the Bit 0 ~ 15 of WRn register is the low word of a read/write value, the Bit 0 ~ 15 of WRn+1 register is the high word of a read/write value. If the read/write address format is set to Rn, only Bit 0 ~ 23 are valid for Rn registers. (Notice: As the Jetter controller is a 24-bit format controller, the valid setting range is 24 Bits (16777215). If setting exceeds this range, HMI will stop read/write operation and show ".....Value is Incorrect" on the screen.
 - 3. When using devices that the data length is in m Words, if the read/write address format is set to WRn, the Bit 0 ~ 15 of WRn register is the lowest word of a read/write value and the Bit 0 ~ 15 of WRn+m-1 register is the highest word of a read/write value. If the read/write address format is set to Rn, the Bit 0 ~ 23 of Rn register is the lowest word of a read/write value and the Bit 0 ~ 23 of Rn register is the highest word of a read/write value and the Bit 0 ~ 23 of Rn register is the highest word of a read/write value. Each register is regards as a "Double Word". The value of Bit24 ~ Bit31 is 0.